

AMENDMENTS TO THE CLAIMS:

Please amend claims 5-8 as shown on the following pages. Material inserted is indicated by underlining (insertion) and material deleted is indicated by strike-out (~~deletion~~).

1. (Original) A manufacturing process for a printed wiring board in which a copper clad laminate obtained by bonding a copper foil on resin as a substrate material is irradiated with carbon dioxide laser light to form a through hole or a hole such as IVH, BVH or the like therein and interlayer connection layer formation is performed, followed by performing circuit formation, characterized by comprising the steps of:
forming a nickel layer as an additional metal layer of 0.08 to 2 μm in thickness on a surface of said copper foil residing in an external layer of said copper clad laminate;
B2
irradiating a surface of said nickel layer with said laser light at a prescribed position where said through hole or said hole such as IVH, BVH or the like is to be formed in said copper clad laminate to thereby remove said nickel layer, said copper foil and said resin layer as a substrate material simultaneously into a desired shape;
using an etching method and a physical polishing method, singly or in combination, to remove said nickel layer as a surface layer of said copper clad laminate after the laser drilling;

performing interlayer connection layer formation to obtain interlayer connection in said copper clad laminate from which said nickel layer is removed; and forming an etching resist layer on a surface of said copper clad laminate after copper plating, followed by exposure, development, circuit etching, and etching resist removal.

2. (Original) A manufacturing process for a printed wiring board in which a copper clad laminate obtained by bonding a copper foil on resin as a substrate material is irradiated with carbon dioxide laser light to form a through hole or a hole such as IVH, BVH or the like therein and interlayer connection layer formation is performed, followed by performing circuit formation, characterized by comprising the steps of:

forming a cobalt layer as an additional metal layer of 0.05 to 3 μm in thickness on a surface of said copper foil residing in an external layer of said copper clad laminate;

B2
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irradiating a surface of said cobalt layer with said laser light at a prescribed position where said through hole or said hole such as IVH, BVH or the like is to be formed in said copper clad laminate to thereby remove said cobalt layer, said copper foil and said resin layer as a substrate material simultaneously into a desired shape;

using an etching method and a physical polishing method, singly or in combination, to remove said cobalt layer as a surface layer of said cooper clad laminate after the laser drilling;

performing interlayer connection layer formation to obtain interlayer connection in said copper clad laminate from which said cobalt layer is removed; and forming an etching resist layer on a surface of said copper clad laminate after copper plating, after copper plating, followed by exposure, development, circuit etching, and etching resist removal.

3. (Original) A manufacturing process for a printed wiring board in which a copper clad laminate obtained by bonding a copper foil on resin as a substrate material is irradiated with carbon dioxide laser light to form a through hole or a hole such as IVH, BVH or the like therein and interlayer connection layer formation is performed, followed by performing circuit formation, characterized by comprising the steps of:

forming a zinc layer as an additional metal layer of 0.03 to 2 μm in thickness on a surface of said copper foil residing in an external layer of said copper clad laminate;

B2
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irradiating a surface of said zinc layer with said laser light at a prescribed position where said through hole or said hole such as IVH, BVH or the like is to be formed in said copper clad laminate to thereby remove said zinc layer, said copper foil and said resin layer as a substrate material simultaneously into a desired shape; using an etching method and a physical polishing method, singly or in combination, to remove said zinc layer as a surface layer of said copper clad laminate after the laser drilling;

performing interlayer connection layer formation to obtain interlayer connection in said copper clad laminate from which said zinc layer is removed; and forming an etching resist layer on a surface of said copper clad laminate after copper plating, followed by exposure, development, circuit etching, and etching resist removal.

4. (Original) The manufacturing process for a printed wiring board according to claim 1, characterized in that an etching solution for use in dissolution of said nickel layer is a selective etching solution not dissolving copper and one of the following solutions is used as said etching solution:
- (1) a sulfuric acid solution of sulfuric acid and nitric acid; and
 - (2) a mixed acid solution of sulfuric acid and nitric acid; and
 - (3) a mixed solution of sulfuric acid, m-nitrobenzenesulfonic acid.
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5. (Currently Amended) A manufacturing process for a printed wiring board in which a copper clad laminate obtained by bonding a copper foil on resin as a substrate material is irradiated with carbon dioxide laser light to form a through hole or a hole such as IVH, BVH or the like therein and interlayer connection layer formation is performed, followed by performing circuit formation, comprising in order the steps of:
- a. forming an organic layer on a surface of said copper foil residing in an external layer of said copper clad laminate;

- b. forming a nickel layer as an additional metal layer of 0.08 to 2 μm in thickness on a surface of said organic layer;
- c. irradiating a surface of said nickel layer with said laser light at a prescribed position where said through hole or said hole such as IVH, BVH, or the like is to be formed in said copper clad laminate to thereby remove said nickel layer, said organic layer, said copper foil and said resin layer as a substrate material simultaneously into a desired shape;
- d. removing said nickel layer from a surface of said copper clad laminate by peeling;
- e. performing interlayer connection layer formation to obtain interlayer connection in the copper clad laminate; and
- f. forming an etching resist layer on said surface of said copper clad laminate after copper plating, followed by exposure, development, circuit etching, and etching resist removal.

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6. (Currently Amended) A manufacturing process for a printed wiring board in which a copper clad laminate obtained by bonding a copper foil on resin as a substrate material is irradiated with carbon dioxide laser light to form a through hole or a hole such as IVH, BVH or the like therein and interlayer connection layer formation is performed, followed by performing circuit formation, characterized by comprising in order the steps of:
 - a. forming an organic layer on a surface of said copper foil residing in an external layer of said copper clad laminate;

- b. forming a cobalt layer as an additional metal layer of 0.05 to 3 μm in thickness on a surface of said organic layer;
- c. irradiating a surface of said cobalt layer with said laser light at a prescribed position where said through hole or said hole such as IVH, BVH, or the like is to be formed in said copper clad laminate to thereby remove said cobalt layer, said organic layer, said copper foil and said resin layer as a substrate material simultaneously into a desired shape;
- d. removing said cobalt layer from a surface of said copper clad laminate by peeling;
- e. performing interlayer connection layer formation to obtain interlayer connection in the copper clad laminate; and
- f. forming an etching resist layer on said surface of said copper clad laminate after copper plating, followed by exposure, development, circuit etching, and etching resist removal.

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7. (Currently Amended) A manufacturing process for a printed wiring board in which a copper clad laminate obtained by bonding a copper foil on resin as a substrate material is irradiated with carbon dioxide laser light to form a through hole or a hole such as IVH, BVH or the like therein and interlayer connection layer formation is performed, followed by performing circuit formation, characterized by comprising in order the steps of:
- a. forming an organic layer on a surface of said copper foil residing in an external layer of said copper clad laminate;

- b. forming a nickel layer as an additional metal layer of 0.08 to 2 μm in thickness on a surface of said organic layer;
- c. irradiating a surface of said nickel layer with said laser light at a prescribed position where said through hole or said hole such as IVH, BVH, or the like is to be formed in said copper clad laminate to thereby remove said nickel layer, said organic layer, said copper foil and said resin layer as a substrate material simultaneously into a desired shape;
- d. performing interlayer connection layer formation to obtain interlayer connection in the copper clad laminate;
- e. removing said nickel layer from a surface of said copper clad laminate by peeling;
- f. forming an etching resist layer on said surface of said copper clad laminate after removal of said nickel layer, followed by exposure, development, circuit etching, and etching resist removal.

B2
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8. (Currently Amended) A manufacturing process for a printed wiring board in which a copper clad laminate obtained by bonding a copper foil on resin as a substrate material is irradiated with carbon dioxide laser light to form a through hole or a hole such as IVH, BVH or the like therein and interlayer connection layer formation is performed, followed by performing circuit formation, characterized by comprising in order the steps of:
- a. forming an organic layer on a surface of said copper foil residing in an external layer of said copper clad laminate;

- b. forming a cobalt layer as an additional metal layer of 0.05 to 3 μm in thickness on a surface of said organic layer;
- c. irradiating a surface of said cobalt layer with said laser light at a prescribed position where said through hole or said hole such as IVH, BVH, or the like is to be formed in said copper clad laminate to thereby remove said cobalt layer, said organic layer, said copper foil and said resin layer as a substrate material simultaneously into a desired shape;
- d. performing interlayer connection layer formation to obtain interlayer connection in the copper clad laminate;
- e. removing said cobalt layer from a surface of said copper clad laminate by peeling; and
- f. forming an etching resist layer on said surface of said copper clad laminate after removal of said cobalt layer, followed by exposure, development, circuit etching, and etching resist removal.

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- 9. (Previously Presented) The manufacturing process for a printed wiring board according to claim 5, characterized in that said organic layer is formed with one or two selected from the group consisting of nitrogen-containing organic compounds, sulfur-containing organic compounds and carboxylic acids.
 - 10. (Previously Presented) The manufacturing process for a printed wiring board according to claim 5, characterized in that said organic layer is formed by repeatedly applying an

organic agent constituted of one or constituted as a mixture of two or more selected from the group consisting of nitrogen-containing organic compounds, sulfur-containing organic compounds and carboxylic acids several times.

11. (Previously Presented) The manufacturing process for a printed wiring board according to claim 5, characterized in that said organic layer is formed by alternately applying two organic agents or more selected from the group consisting of nitrogen-containing organic compounds, sulfur-containing organic compounds and carboxylic acids several time.
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